

# SPECIFICATION







## 1.4 Package Dimension

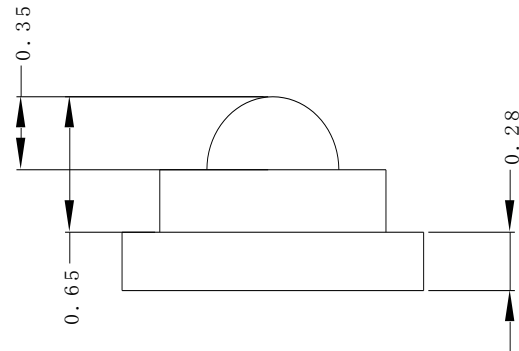
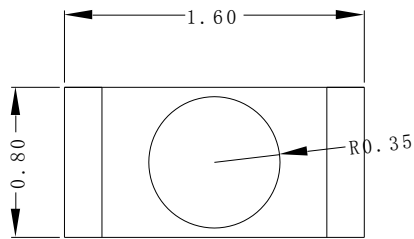


Fig.1-1 Top view

Fig.1-2 Side view

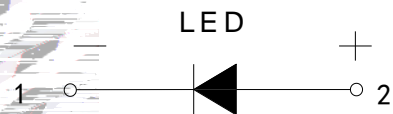
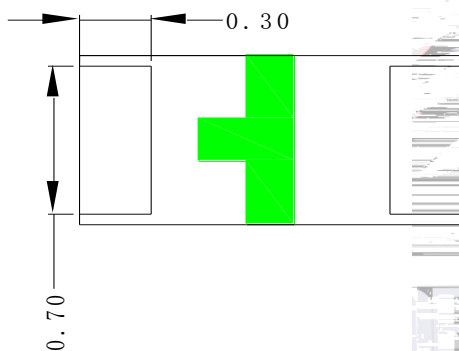


Fig.1-3 Bottom view

Fig.1-4 Polarity

### Notes

1. All dimensions units are millimeters.

All dimensions tolerances are  $\pm 0.2\text{mm}$  unless otherwise noted.

0.2

## 1.5 Product Parameters

Table 1-1 Electrical / Optical Characteristics at Ts=25°C

Item	Test Condition	Symbol	Value			Unit	
			Min	Typ	Max		
Spectral Half Bandwidth	I <sub>F</sub> =20mA	Δλ	--	15	--	nm	
Forward Voltage	I <sub>F</sub> =20mA	V <sub>F</sub>	E0	2.4	--	2.6	V
			F0	2.6	--	2.8	V
			G0	2.8	--	3.0	V
			H0	3.0	--	3.2	V
			I0	3.2	--	3.4	V
		J0	3.4	--	3.6	V	
		D00	515	--	520	nm	
		E00	520	--	525	nm	
		F00	525	--	530	nm	
		G00	530	--	535	nm	
Luminous Intensity	I <sub>F</sub> =20mA	I <sub>v</sub>	J00	350	--	530	mcd
			K00	530	--	800	mcd
			L00	800	--	1200	mcd
Viewing Angle at 50% I <sub>v</sub>	I <sub>F</sub> =20mA	2θ <sub>1/2</sub>	--	60	--	deg	
Reverse Current	V <sub>R</sub> =5V	I <sub>R</sub>	--	--	10	μA	
Thermal Resistance	I <sub>F</sub> =20mA	R <sub>THJ-S</sub>	--	--	450	°W	

Note :V<sub>R</sub>=5V For test conditions.V<sub>R</sub>=5V

Table 1-2 Absolute Maximum Ratings at Ts=25°C

Parameter	Symbol	Rating	Units
Power Dissipation	$P_d$	108	mW
Forward Current	$I_F$	30	mA
Peak Forward Current Of Pulse	$I_{FP}$	60	mA
Electrostatic Discharge (HBM)	$E_{SD}$	1000	V
Operating Temperature	$T_{opr}$	-40 ~ +85	
Storage Temperature	$T_{stg}$	-40 ~ +85	
Junction Temperature	$T_j$	95	

Notes

- 1/10 Duty cycle, 0.1ms pulse width. 0.1ms, 1/10.
- The above forward voltage measurement allowance tolerance is  $\pm 0.1V$ .
- The above dominant wavelength measurement allowance tolerance is  $\pm 2nm$ . 2nm.
- The above luminous intensity measurement allowance tolerance  $\pm 10\%$ . 10%.
- Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
- All measurements were made under the standardized environment of Refond.
- When the LEDs are in operation the maximum current should be decided after measuring the package temperature, junction temperature should not exceed the maximum rate. LED

## 1.6 Typical optical characteristics curves

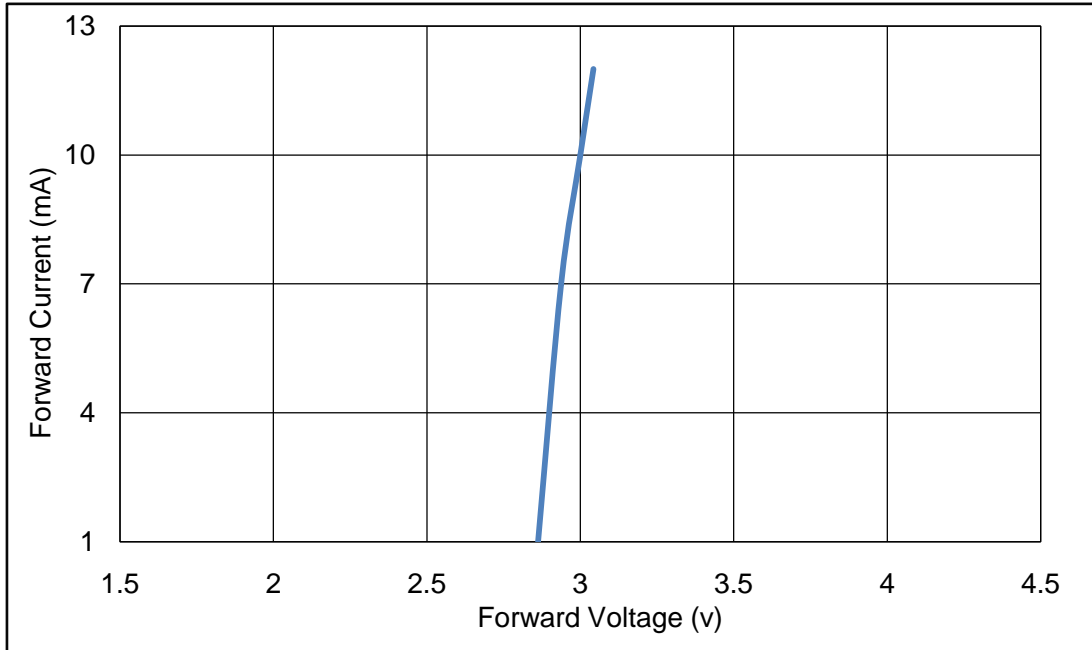


Fig 1-6 Forward Voltage Vs. Forward Current

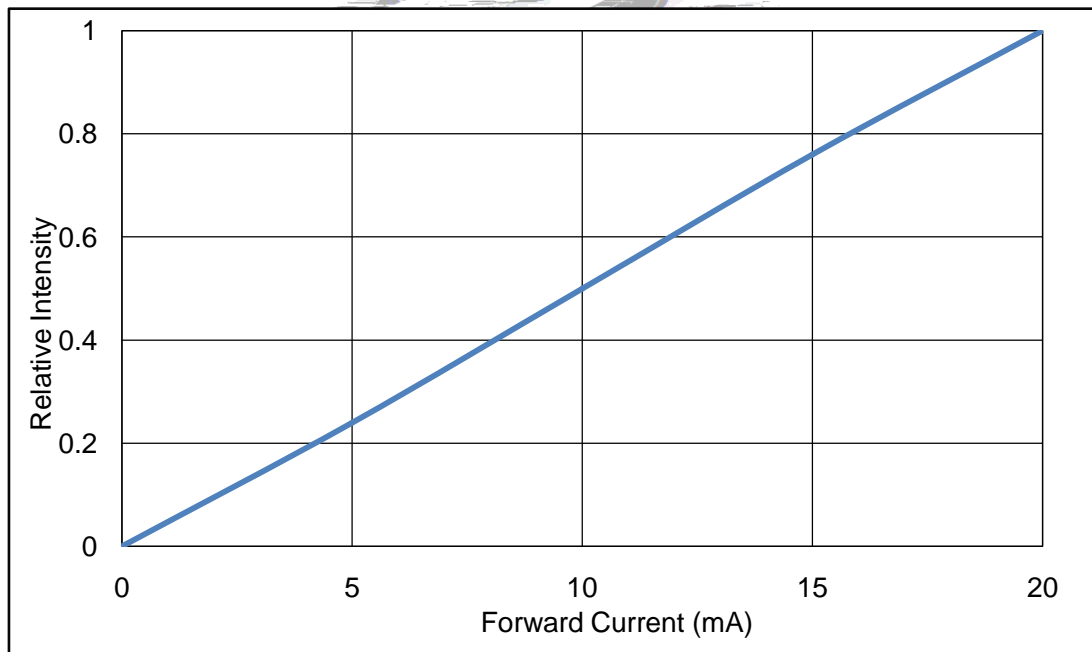


Fig 1-7 Forward Current Vs. Relative Intensity

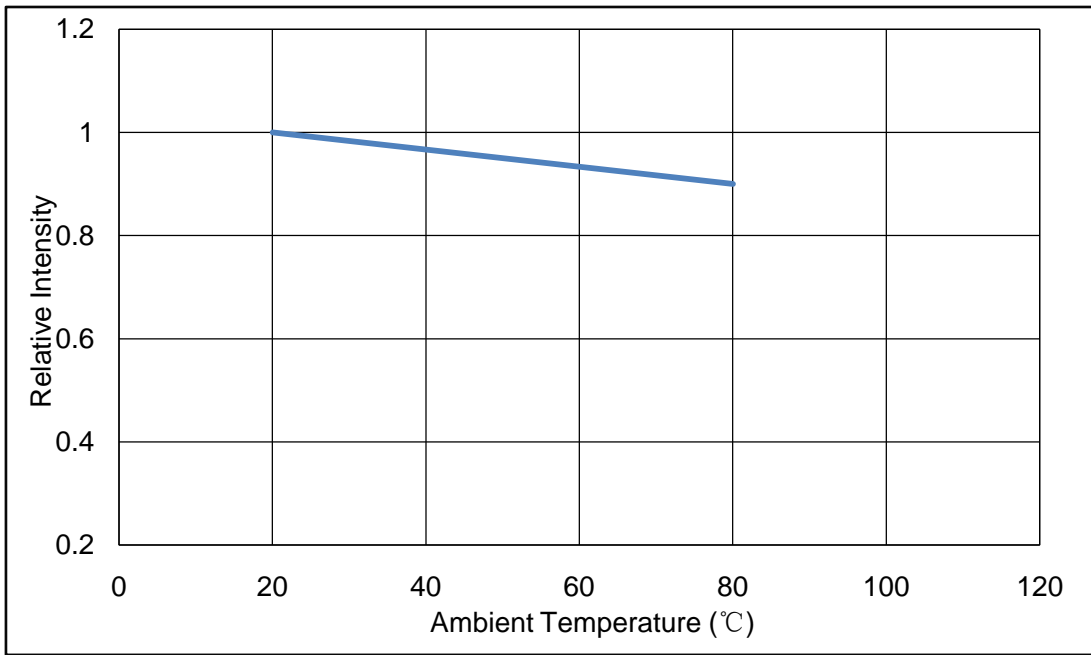


Fig 1-8 Solder Temperature Vs Relative Intensity

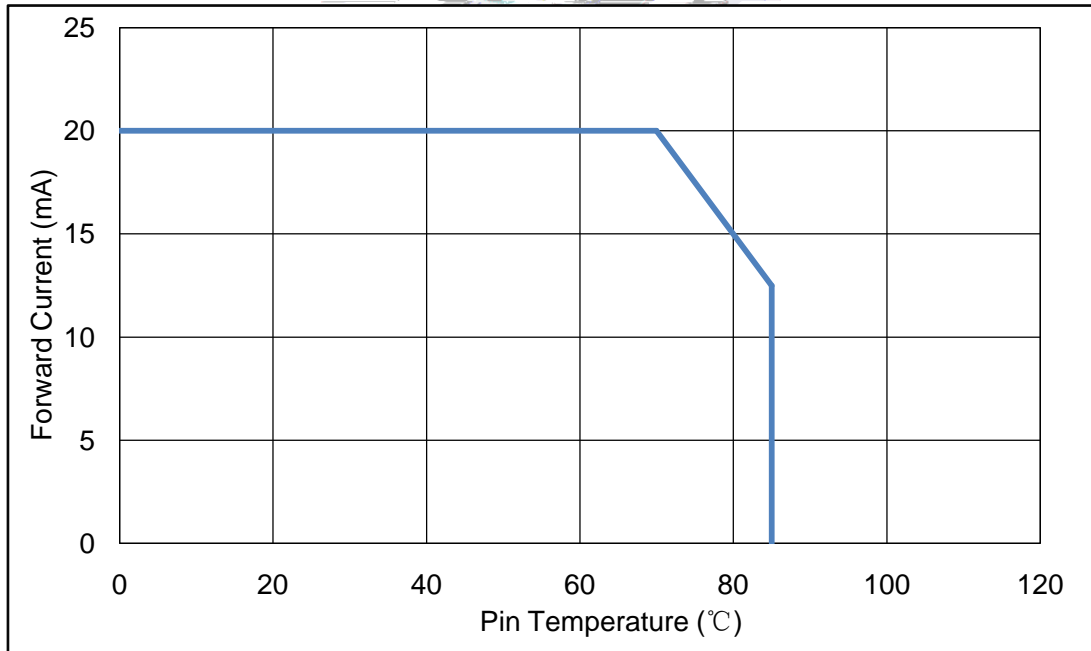
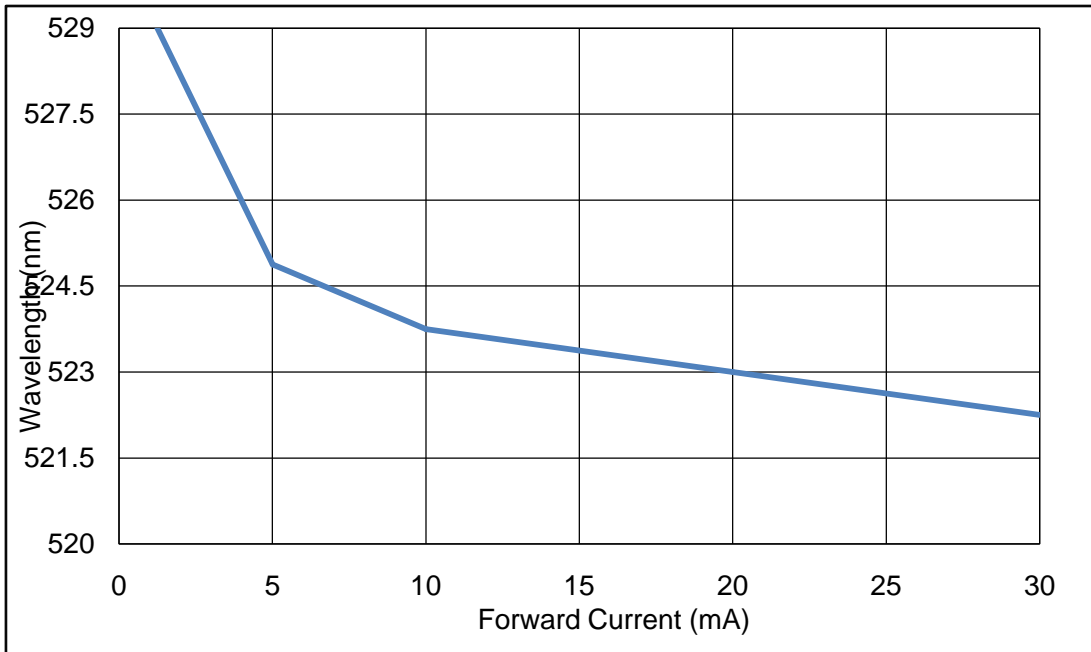


Fig 1-9 Solder Temperature Vs Forward Current



°C 正向电流与主波长关系曲线

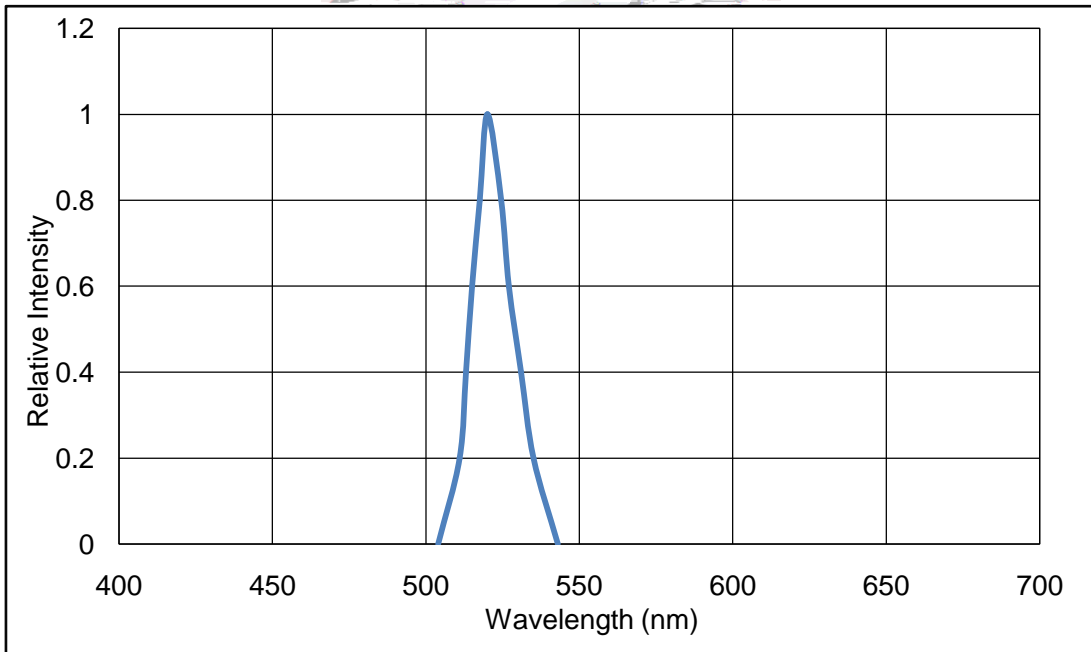


Fig.1-12 Relative Intensity Vs. Wavelength (Ta=25°C)

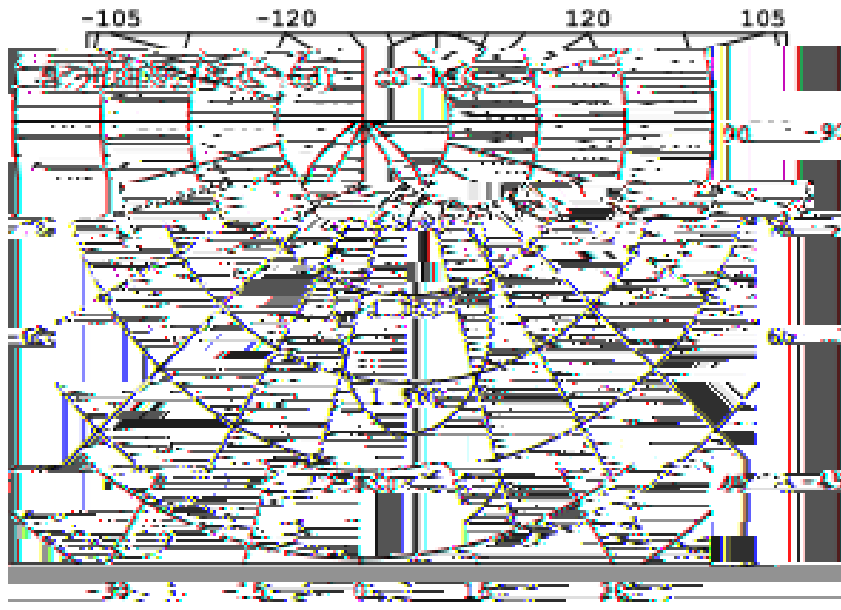
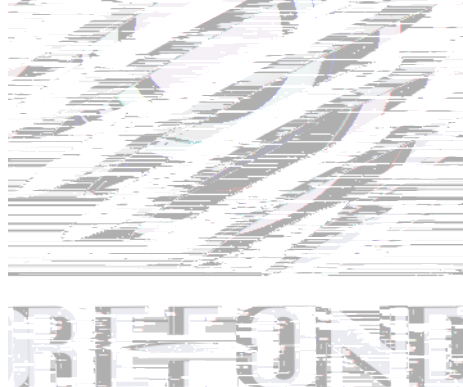


Fig.1-13 Diagram characteristics of radiation



## 2. Packaging

### 2.1 Packaging Specification

Package:4000pcs/reel.

4000pcs

#### 2.1.5 Carrier Tape Dimension

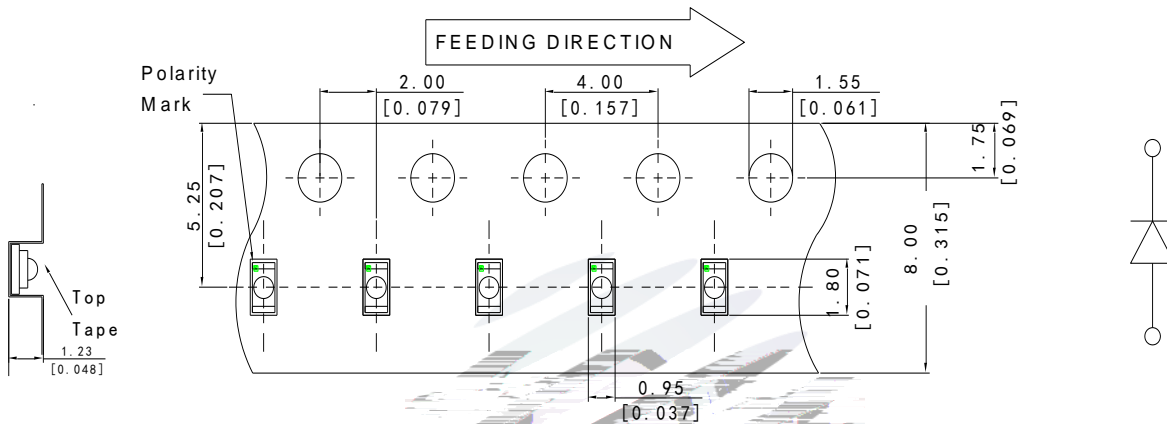


Fig.2-1 Carrier Tape Dimension

#### 2.1.2 Reel Dimension

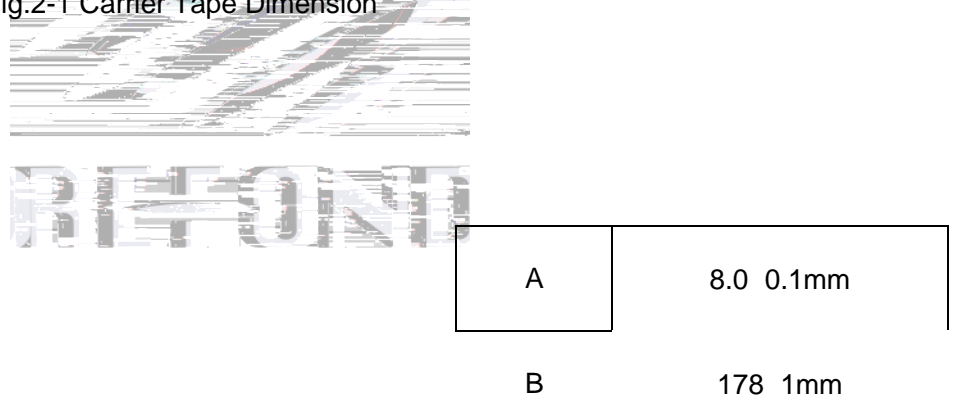


Fig 2-2 Reel Dimension

Notes The tolerances unless mentioned  $\pm 0.1$ mm. Unit : mm



## 2.3 Cardboard Box

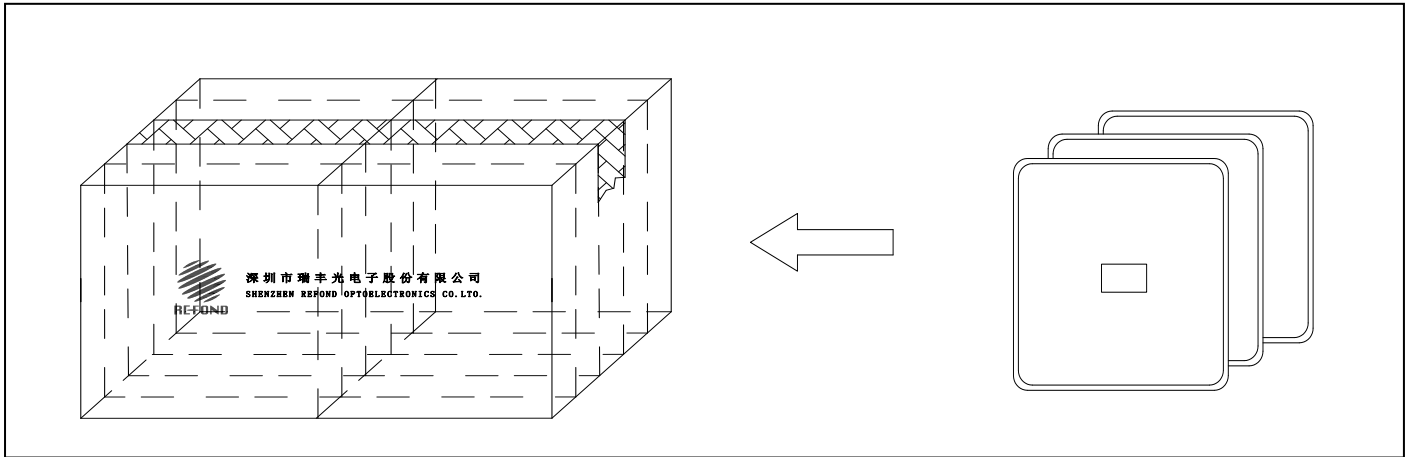


Fig 2-5 Cardboard Box

## 2.4 Reliability Test Items And Conditions

Table 2-3 Reliability Test Items And Conditions

Test Items	Ref.Standard	Test Condition	Time	Quantity	Ac/Re /
Reflow	JESD22-B106	Temp:260 max T=10 sec	2 times	22Pcs	0/1
Temperature Cycle	JESD22-A104	100 30 min ↑↓5 min -40 30 min	300 cycles	22Pcs	0/1
Thermal Shock	JESD22-A106	-40 15min ↑↓ 100 15min	300 cycles	22Pcs	0/1
High Temperature Storage	JESD22-A103	T <sub>emp</sub> :100	1000 hrs	22Pcs	0/1
Low Temperature Storage	JESD22-A119	T <sub>emp</sub> :-40	1000 hrs	22Pcs	0/1
Life Test	JESD22-A108	T <sub>a</sub> =25 I <sub>f</sub> =20mA	1000 hrs	22Pcs	0/1

## 2.5 Criteria For Judging Damage

Table 2-4Criteria For Judging Damage

Test Items	Symbol	Test Condition	Criteria For Judgement	
			Min.	Max.
Forward Voltage	$V_F$	$I_F=20\text{mA}$	-	U.S.L*)x1.1
Reverse Current	$I_R$	$V_R= 5\text{V}$	-	U.S.L*)x2.0
Luminous Flux	$\Phi$	$I_F=20\text{mA}$	L.S.L*)x0.7	-

### Notes

- 1.U.S.L: Upper standard level                      L.S.L: Lower standard level
- 2.The Reliability tests are based on Refond existing test platform.
- 3.The technical information shown in the data sheets are limited to the typical characteristics and circuit examples of the referenced products. It does not constitute the warranting of industrial property nor the granting of any license.

### 3. SMT Reflow Soldering Instructions SMT

#### 3.1 SMT Reflow Soldering Instructions SMT

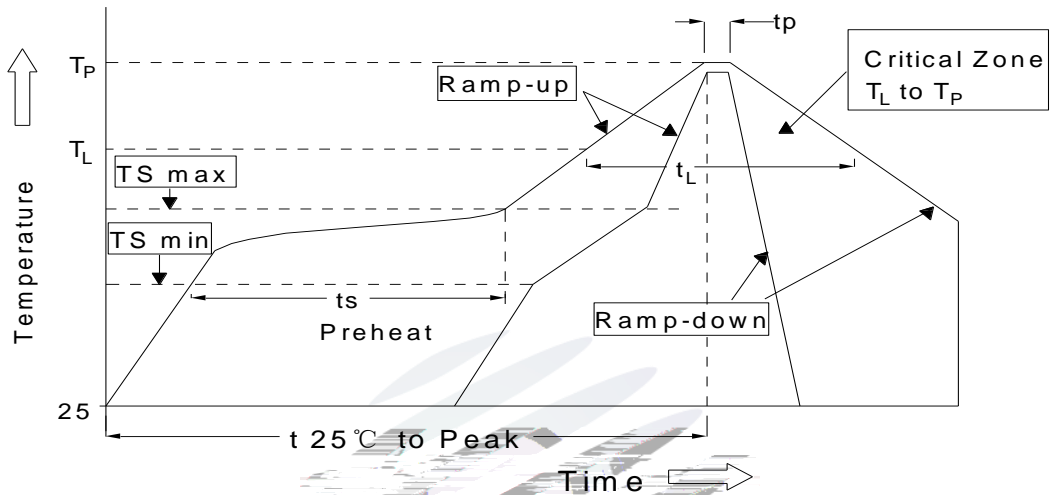


Fig.3-1 SMT Reflow Soldering Instructions SMT 回流焊说明

Table 3-1 Parameter 参数

Tsmax	TP	3 °C/
(Tsmín)		150 °C
(Tsmáx)		200 °C
Tsmín	Tsmáx	60 - 120
(TL)		217 °C
(tL)		60
/	(TP)	260 °C
	tp	10
(TP)	5 °C	30
		6 °C/
25 °C		8



temperature after soldering. Do not rapidly cool device after soldering.

## 4. Handling Precautions

### 4.1 Handling Precautions

(1) LED operating environment and sulfur element composition cannot be over 100PPM in the LED mating usage material. This is provided for informational purposes only and is not a warranty or endorsement.

LED	LED	100PPM.
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(2) In order to prevent external material from getting into the inside of LED, which may cause the malfunction of LED, the single content of Bromine element is required to be less than 900PPM, the single content of Chlorine element is required to be less than 900PPM, the total content of Bromine element and Chlorine element in the external materials of the application products is required to be less than 1500PPM. This is provided for informational purposes only and is not a warranty or endorsement.

LED	LED	900PPM	900PPM
1500PPM.			

(3) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture. Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues. Refond advises against the use of any chemicals or materials that have been found or are suspected to have an adverse affect on device performance or reliability. To verify compatibility, Refond recommends that all chemicals and materials be tested in the specific application and environment for which they are intended to be used. Attaching LEDs, do not use adhesives that outgas organic vapor.

LED

LED

LED

(4) Handle the component along the side surface by using forceps or appropriate tools; do not directly touch or Handle the silicone lens surface, it may damage the internal circuitry.

(5) In designing a circuit, the current through each LED must be exceed the absolute maximum rating specified for each LED. In the meanwhile, resistors for protection should be applied, otherwise slight voltage shift will cause big current change, burn out may happen. The driving circuit must be designed to allow forward voltage only when it is ON or OFF. If the reverse voltage is applied to LED, migration can be generated resulting in LED damage. LED

LED

(6) Thermal Design is paramount importance because heat generation may result in the Characteristics decline, such as brightness decreased, Color change and so on. Please consider the heat generation of the LEDs when making the system design. LED

LED

(7) Storage

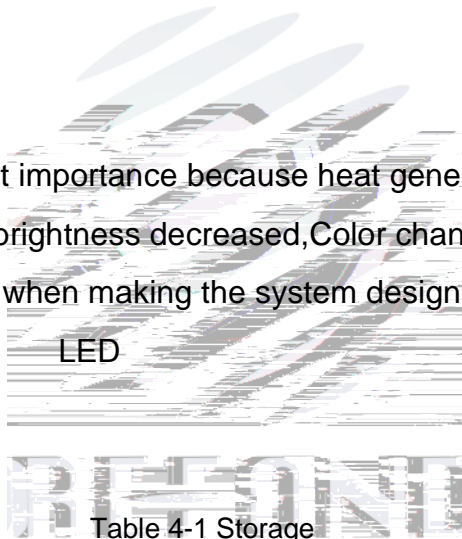


Table 4-1 Storage

Conditions		Temperature	Humidity	Time
Storage	Before Opening Aluminum Bag	≤30℃	≤75%	Within 1 Year from Delivery Date
	After Opening Aluminum Bag	≤30℃	≤60%	168hours 168
Baking		60±5℃	-	≥24hours 24

(8) If the moisture absorbent material ( silica gel ) has faded away or the LEDs have exceeded the storage time , baking treatment should be performed after unpacking and based on the

following condition (60±5) °C for above 24 hours.

60 5 24

(9) If the package is flatulence or damaged, please notify the sales staff to assist.

(10) Similar to most Solid state devices; LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS). LED

(11) Other points for attention, please refer to our relevant information.







Declare

This specification is written both in English and in Chinese and the latter is formal.